IN THE CLAIMS

Please cancel claims 2-4 and 29-31 without prejudice or disclaimer and amend the claims as follows:

1. (Currently Amended)   A method comprising:

detecting a power management event; and

changing a display update property for a video display in response to the power management event, the display update property comprising at least one of a screen resolution or a pixel depth.

2-4. (Canceled)

5. (Original)   The method of claim 1, wherein the detecting the power management event includes detecting a change in a power source from AC power to DC power and wherein changing the display update property includes decreasing the display update property.

6. (Original)   The method of claim 1, wherein the detecting the power management event includes detecting a change in a power source from DC power to AC power and wherein changing the display update property includes increasing the display update property.

7. (Original)   The method of claim 1, wherein the detecting the power management event includes detecting a decrease in a power level of a battery past a predetermined threshold and wherein changing the display update property includes decreasing the display update property.

8. (Original)   The method of claim 1, wherein the detecting the power management event includes detecting an increase in a power level of a battery past a predetermined threshold and wherein changing the display update property includes increasing the display update property.

9. (Original)   The method of claim 1, further comprising determining if a policy exists for the power management event and wherein changing the display update property includes changing the display update property in accordance with the policy.

10. (Original) A system comprising:

      a processor;

      a graphics controller coupled to the processor; and

      a frame buffer coupled to the graphics controller, said frame buffer having a size corresponding to a screen resolution and a pixel depth;

      wherein the processor is operable to:

            detect a power management event; and

            change the screen resolution in response to the power management event.

11. (Original) The system of claim 10, wherein the power management event includes a change in a power source from AC power to DC power and further wherein the processor is operable to decrease the screen resolution.

12. (Original) The system of claim 10, wherein the power management event includes a change in a power source from AC power to DC power and further wherein the processor is operable to decrease the pixel depth.

13. (Original) The system of claim 10, wherein the power management event includes a change in a power source from DC power to AC power and wherein the processor is operable to increase the screen resolution.

14. (Original) The system of claim 10, wherein the power management event includes a change in a power source from DC power to AC power and further wherein the processor is operable to increase the pixel depth.

15. (Currently Amended)   The system of claim 10, wherein the power management event includes a decrease in a power level of a battery past a predetermined threshold and wherein the processor is operable to ~~decreasing~~ decrease the screen resolution.

16. (Original) The system of claim 10, wherein the power management event includes an increase in a power level of a battery past a predetermined threshold and wherein the processor is operable to increase the screen resolution.

17. (Original) The system of claim 10, wherein the processor is further operable to determine if a policy exists for the power management event and wherein the processor is operable to change the screen resolution in accordance with the policy.

18. (Original) The system of claim 10 further comprising:

    a memory controller; and

    a system memory coupled to the memory controller;

    wherein the frame buffer resides in the system memory.

19. (Original) The system of claim 18, wherein the graphics controller and the memory controller are integrated into a single chipset.

20. (Currently Amended)   A system comprising:

    a processor;

    a frame buffer; and

    a graphics controller coupled to the processor and the frame buffer, ~~said graphics controller updating a video display from~~ the frame buffer ~~according to a refresh rate~~ having a size corresponding to a screen resolution and a pixel depth;

    wherein the processor is operable to:

        detect a power management event; and

        change the ~~refresh rate~~ pixel depth in response to the power management event.

21. (Currently Amended)   The system of claim 20, wherein the power management event includes a change in a power source from AC power to DC power and wherein the processor is operable to decrease the ~~refresh rate~~ pixel depth.

22. (Currently Amended)   The system of claim 20, wherein the power management event includes a change in a power source from DC power to AC power and wherein the processor is operable to increase the ~~refresh rate~~ pixel depth.

23. (Original) A graphics controller comprising:

    a processor;

    a frame buffer coupled to the processor;

    wherein the processor is operable to:

        receive a power management event; and

        change a screen resolution for a video display refreshed from the frame buffer in

    response to the power management event.

24. (Currently Amended)   The graphics controller of claim 23, wherein the power management event includes [[an]] a change in a power source from AC power to DC power and wherein the processor is operable to decrease the screen resolution.

25. (Original) The graphics controller of claim 24, wherein the processor is further operable to decrease a pixel depth for the frame buffer.

26. (Original) The graphics controller of claim 23, wherein the power management event includes a change in a power source from DC power to AC power and wherein the processor is operable to increase the screen resolution.

27. (Original) The graphics controller of claim 26, wherein the processor is further operable to increase a pixel depth for the frame buffer.

28. (Currently Amended)   A machine-readable medium having machine executable instructions for performing a method comprising:

    detecting a power management event; and

    changing a display update property for a video display in response to the power management event, the display update property comprising at least one of a screen resolution or a pixel depth.

29-31. (Canceled)

32. (Original) The machine readable medium of claim 28, wherein the detecting the power management event includes detecting a change in a power source from AC power to DC power and wherein changing the display update property includes decreasing the display update property.

33. (Original) The machine readable medium of claim 28, wherein the detecting the power management event includes detecting a change in a power source from DC power to AC power and wherein changing the display update property includes increasing the display update property.

34. (Original) The machine readable medium of claim 28, wherein the detecting the power management event includes detecting a decrease in a power level of a battery past a predetermined threshold and wherein changing the display update property includes decreasing the display update property.

35. (Original) The machine readable medium of claim 28, wherein the detecting the power management event includes detecting an increase in a power level of a battery past a predetermined threshold and wherein changing the display update property includes increasing the display update property.

36. (Original) The machine readable medium of claim 28, further comprising determining if a predefined policy exists for the power management event and wherein changing the display update property includes changing the display update property in accordance with the predefined policy.